

### III. REMARKS

1. Claims 6, 7, 8, 11, 12, and 17 are amended. Claim 13 is cancelled without prejudice. Claims 21-22 are new. The specification and drawings are amended.

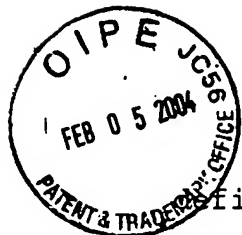
2. Applicants appreciate the Examiner's indication of the allowability of claims 1-6. Claim 6 is amended to correct the punctuation error. The change does not further limit or narrow the scope of the claim and is not made for reasons related to patentability.

3. FIGS. 1 and 2 are corrected to be designated by the legend "Prior Art." New formal drawings with that change are provided herewith.

4. The claim objections to claims 6, 8, 11 and 17-20 are addressed by the changes to those claims. These changes do not further limit or narrow the scope of the claims and are not made for reasons related to patentability.

5. Claims 7, 12 and 17 are amended to include the feature that a certain amount of chip material is removed from adjacent ends of LED chips in the array in order to maintain the gap needed for the constant pitch required for the array of Applicants' invention. It is respectfully submitted that this feature is not disclosed or suggested by Matsushita or Kahen et al. Thus, it is submitted the claims, as amended, are not disclosed or suggested by Matsushita or Kahen et al., or the combination thereof (35 U.S.C. §102(b) and §103(a)).

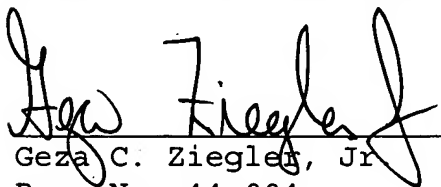
It is noted that although Applicants provided FIG. 2 as an illustration of a narrow gap, approximately 5 $\mu$ m, between adjacent chips, Applicants clearly state that this architecture is



efficient when it comes to a 1200 SPI array. The reason for this, as described in the specification is because "the pitch 29 between adjacent pixels on different chips is significantly larger than the average pitch 25." (Pg. 7, lines 7-9) It is stated in the specification, at pg. 7, line 9, that "[t]his is undesirable." Thus, the configuration of FIG. 2 does not achieve the desired results of Applicants' invention, and is deficient in key respects. Therefore, it is submitted that FIG. 2 is not prior art for the gap of 5 $\mu$ m.

A check in the amount of \$18.00 is enclosed for the additional claim fee. The Commissioner is hereby authorized to charge payment for any fees associated with this communication or credit any over payment to Deposit Account No. 16-1350..

Respectfully submitted,

  
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Reg. No. 44,004

28 JANUARY 2004  
Date

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